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cc  
9/19/02

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yasuhiro WAKIMOTO

Art Unit: 2186

Application No.: 09/652,023

Examiner: Choi, Woo H.

Filed: August 31, 2000

Attorney Dkt. No.: 108391-00011

For: MICROPROCESSOR AND MEMORY DEVICE

RECEIVED

RESPONSE UNDER 37 C.F.R § 1.121

SEP 18 2002

Technology Center 2100

Commissioner for Patents  
Washington, D.C. 20231

Date: September 13, 2002

Sir:

In response to the Office Action dated May 13, 2002, the response period being extended from August 13, 2002 to September 13, 2002 by the attached petition please consider the following amendments and remarks as a full and complete response thereto.

IN THE CLAIMS:

Please add claims 17-18 as follows:

17. (New) The microprocessor of claim 1, wherein the operating speed of the second memory unit is faster than the operating speed of the first memory unit.

18. (New) The microprocessor of claim 1, wherein the access speed of the second memory unit is faster than the operating speed of the first memory unit.